

①

Semiconductors

Solids categorized by electrical conductivity,

CPU's,
PROM's, FPGA
diodes,
transistors

<u>Conductors</u>	$\rho (\Omega m)$
metals (Cu)	$\approx 10^{-7}$
alloys (Nichrome)	$\sim 10^{-6}$

Semiconductors

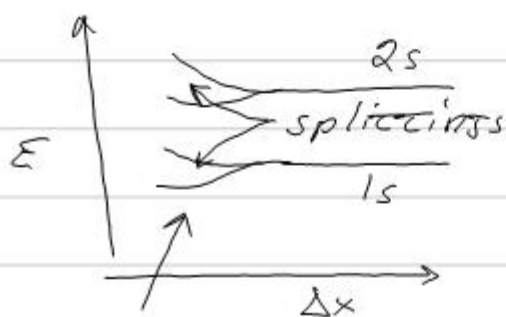
Silicon, Ge $10^{-4} - 10^3$

Insulators

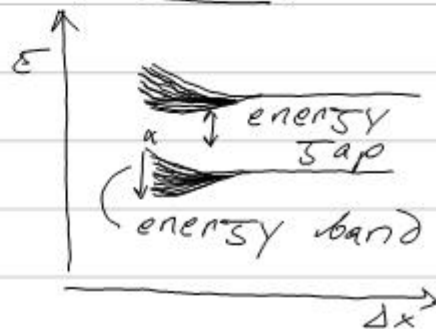
Rubber $\approx 10^8$

Can be understood by considering impact of large #s of atoms on individual wave functions.

2 atoms:



11 atoms:

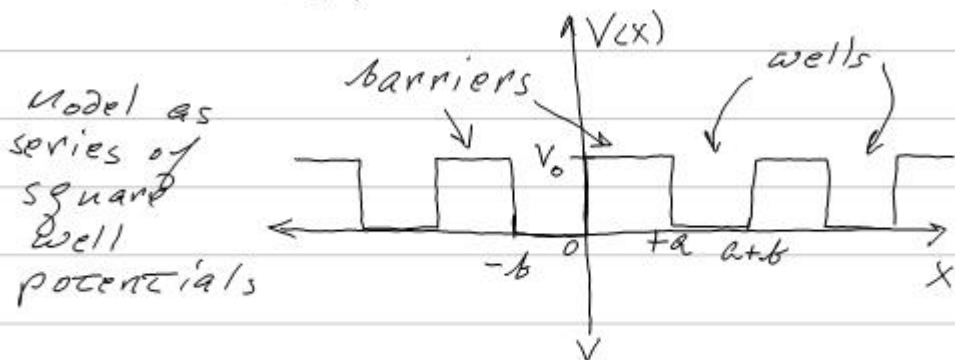


nearly potential well causes splittings

Kronig-Penney Model :

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Consider an e^- in a lattice of atoms:



In a particular well, free particle

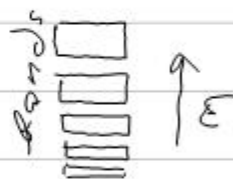
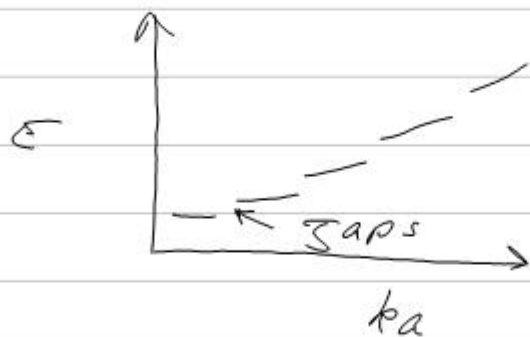
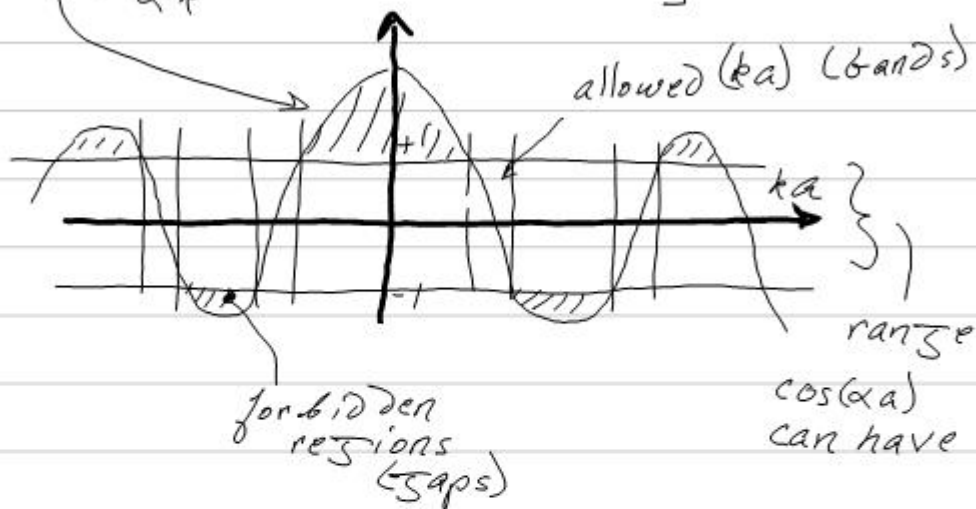
$$\psi(x) = A e^{ikx} + B e^{-ikx} \quad k^2 = \frac{2mE}{\hbar^2}$$

In each barrier ($E < V_0$)

$$\psi(x) = C e^{\alpha x} + D e^{-\alpha x} \quad \alpha^2 = \frac{2m(V_0 - E)}{\hbar^2}$$

Boundary conditions give relation (3)

$$\left[\frac{\alpha^2 b}{2k} \sin(ka) + \cos(ka) \right] = \cos(\alpha a)$$



Band Theory helps classify materials ④

unfilled



conductors

- easy to move bound e^- s into very high level "conduction band"

- no 'gap' in way of e

cond. band



insulators

- large (several eV) gap between conduction + valence bands
- cannot get e^- s into conduction band

valence band



cond. band



Semiconductors

- valence + conduction bands close
 $\Delta E \sim 1 \text{ eV}$
- can get modest current

valence band



Fermi - Statistics & Hole Generation

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Fermi-Dirac statistics gives a scale factor to the # density of states

$$F_{FD} = \left[e^{(\epsilon - \epsilon_F)/kT} + 1 \right]^{-1}$$

As T rises:

- increasing fraction of time
- e^- in conduction band ($\epsilon > \epsilon_F$)
- so ρ decreases $\rightarrow$ as observed

Holes:

- e^- moving along in conduction band



- leaves behind positively charged 'hole' in valence band
- intrinsic semiconductor

$$N_{-CB} = N_{+CB}$$

n-Type Semiconductors

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of conduction e^- s controlled by exponential part of F_D
very few $\Rightarrow$ small current

Doping: add a small quantity of certain atoms

- can enhance the e^- that are in conduction band
- will have e^- that are very close to conduction band

Example: Arsenic $4s^2 4p^3$



5th e^- is
0.05 eV from
conduction
band

↓
'donor e^- '

"negative-type"

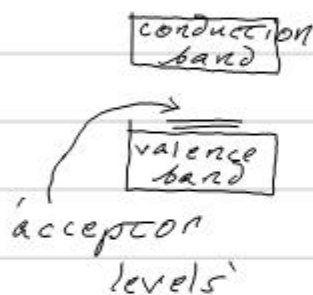
p-Type Semiconductor

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One could also add a dopant having fewer e^- than Si

Example: Indium $5s^2 5p^1$

→ there's an extra hole per Indium atom



- E levels just above valence band

("acceptor levels")

- easily accept e^- from valence band

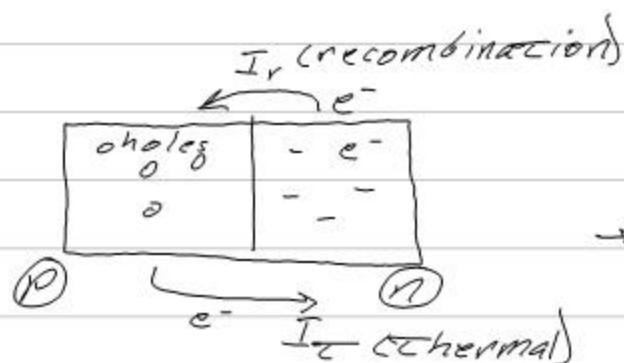
When E -field applied: doping causes an increase in current (decrease in R)

→ observe apparent flow of holes in direction of $\vec{E}$
"positive"-type

p-n Junction Diodes

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Join a p-type + n-type semiconductor



No Bias Case

$$I_c = -I_r$$

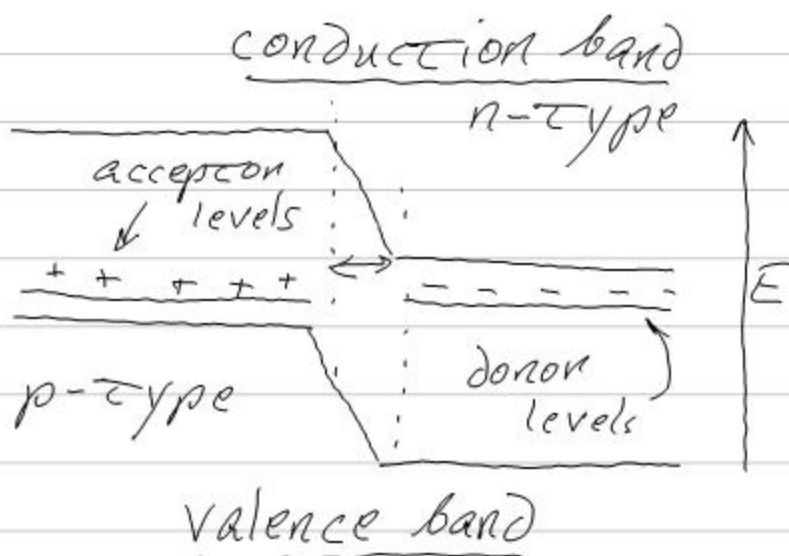
- e^- on n-side want to "recombine" with holes in p-side: I_r
- small current from thermal e^- which get into acceptor band: $I_{thermal}$

Energy Bands In a Diode:

No Bias

Acceptor + Donor levels

→ @ same E



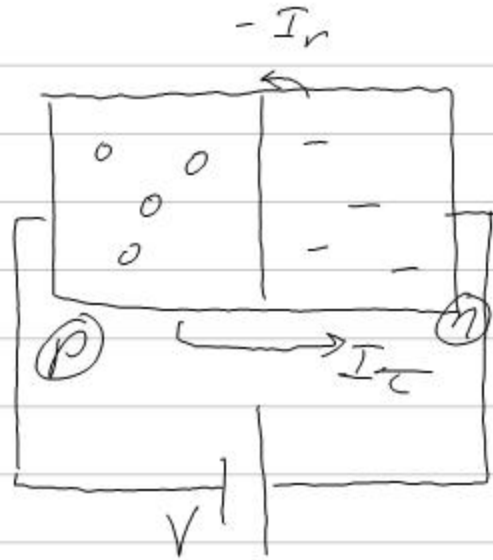
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Reverse Bias

I_r reduced due to potential difference

I_c remains same

$$|I_c| > |I_r|$$



Very small net flow of e^- s from P to N: I in opposite direction

Reverse Bias

e^- in n-type

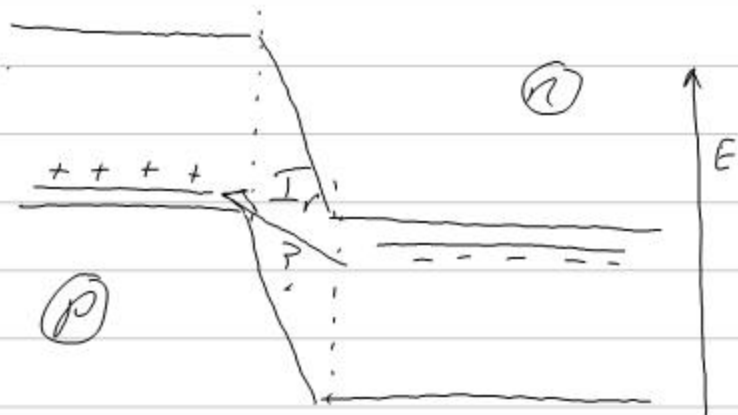
no must pass an Energy

"hill" to

set to

p-type acceptors

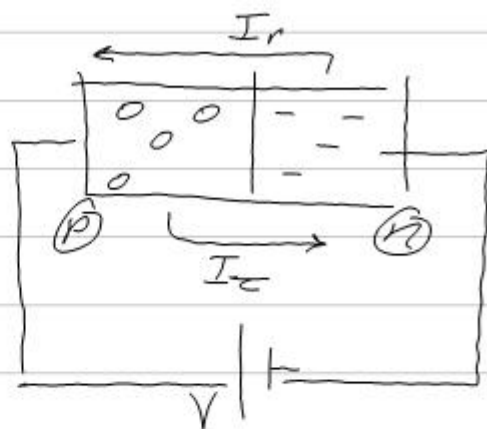
$\therefore I_r$ is small



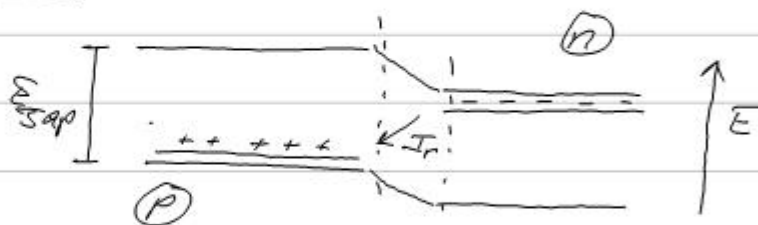
Forward Bias

I_C same small value

I_r much larger;
 $\rightarrow e^-$ already want to go this way



$|I_r| \gg |I_C| \rightarrow \text{total } I \text{ large}$
going from N $\rightarrow$ P

Forward Bias

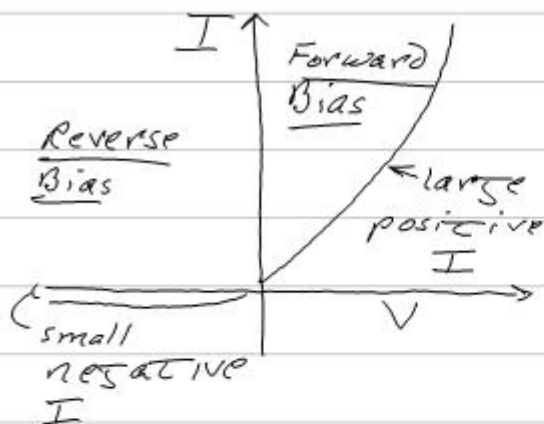
E barrier is gone

- Now e^- donors move thru conduction band and deexcite to acceptor levels
 $\rightarrow$ can generate heat or light

L (light) E (emitting) D (diodes)

$E_g = \text{gap energy, } E_{\text{gap}}$

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@ room Temperature:

$$I_{n \rightarrow p}^{\text{no bias}} \propto N e^{-eV_0/kT} = I_0$$

(# in conduction band)

Forward Bias:

$$I \propto N e^{-e(V-V_0)/kT}$$

$$= I_0 e^{eV/kT}$$

- plus motion of holes ($= -I_0$)

$$I_{\text{total}} = I_0 (e^{eV/kT} - 1)$$

Example Use of Diodes:

Rectification (AC $\rightarrow$ DC power)

