

## Selected recent papers

1. "Response of a 0.25  $\mu\text{m}$  thin-film silicon-on-sapphire CMOS technology to total ionizing dose." M.P. King, *et al.*, 2010. Prepared for 2010 Topical Workshop on Electronics for Particle Physics (TWEPP-10), Aachen, Germany, 20-24 Sep 2010. Published in JINST 5:C11021, 2010.
2. "A 16:1 Serializer ASIC for Data Transmission at 5 Gbps", D. Gong *et al.*, Prepared for 2010 Topical Workshop on Electronics for Particle Physics (TWEPP-10), Aachen, Germany, 20-24 Sep 2010. Published in 2010 JINST 5 C12009.
3. "A 4.9-GHz Low Power, Low Jitter, LC Phase Locked Loop". T. Liu *et al.*, Prepared for 2010 Topical Workshop on Electronics for Particle Physics (TWEPP-10), Aachen, Germany, 20-24 Sep 2010. Published in 2010 JINST 5 C12045.
4. "R&D towards cryogenic optical links." T. Liu *et al.* 2010. 11pp. Prepared for 2010 Topical Workshop on Electronics for Particle Physics (TWEPP-10), Aachen, Germany, 20-24 Sep 2010. Published in JINST 5:C12030, 2010.
5. "The radiation tolerance of specific optical fibres exposed to 650 kGy(Si) of ionizing radiation", B. Arvidsson *et al.*, JINST 4 P07010 (2009).
6. "The versatile link, a common project for super-LHC", L. Amaral *et al.* JINST 4 P12003 (2009)